LAB #07

Generating unit impulse and unit step sequences,

Basic signal operations



Spring 2023

**CSE-301L Signals & Systems Lab**

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Registration No.: 21PWCSE2028

Section: C

“On my honor, as a student of the University of Engineering and Technology, I have neither given nor received unauthorized assistance on this academic work”

Submitted to:

Engr. Sumayyea

(19 May 2023)

Department of Computer systems engineering

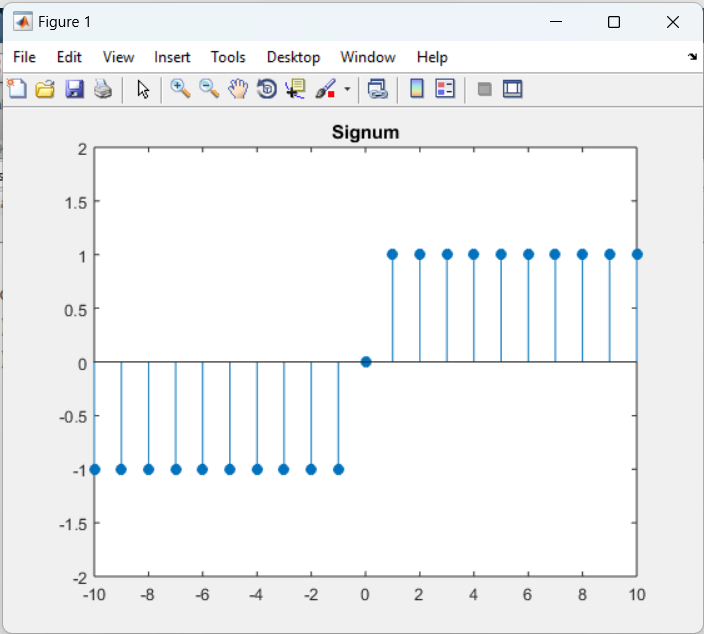
University of Engineering and Technology, Peshawar

**Task 1**

**Code:**

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**Output:**

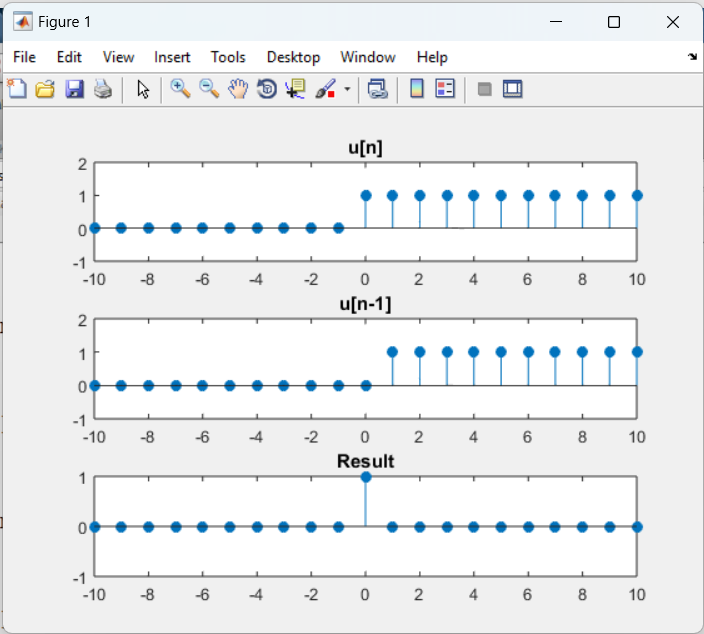
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**Task 2**

**Code:**

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**Output:**

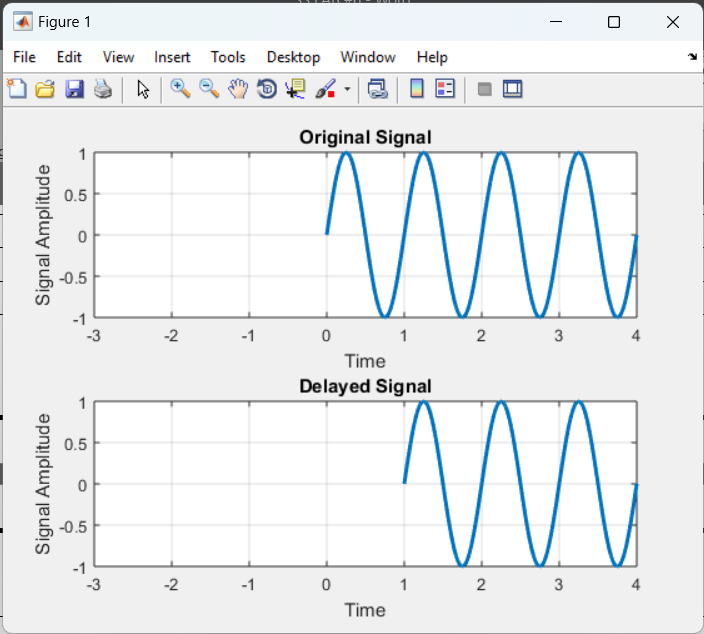


**Task 3**

**Code:**

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**Output:**

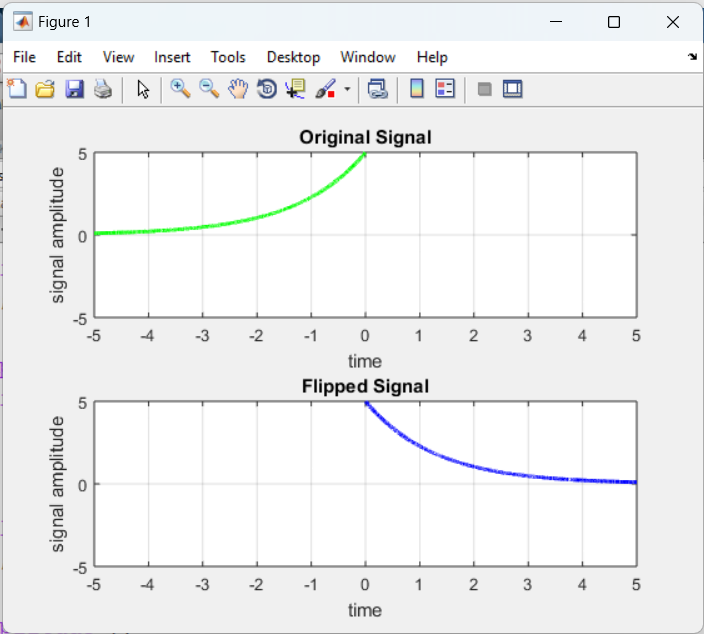
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**Task 4**

**Code:**

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**Output:**

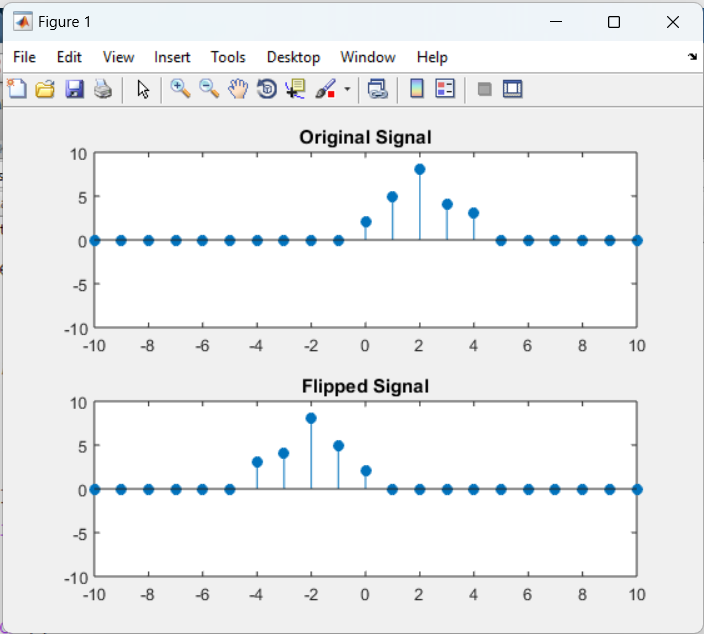
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**Task 5**

**Code:**

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**Output:**

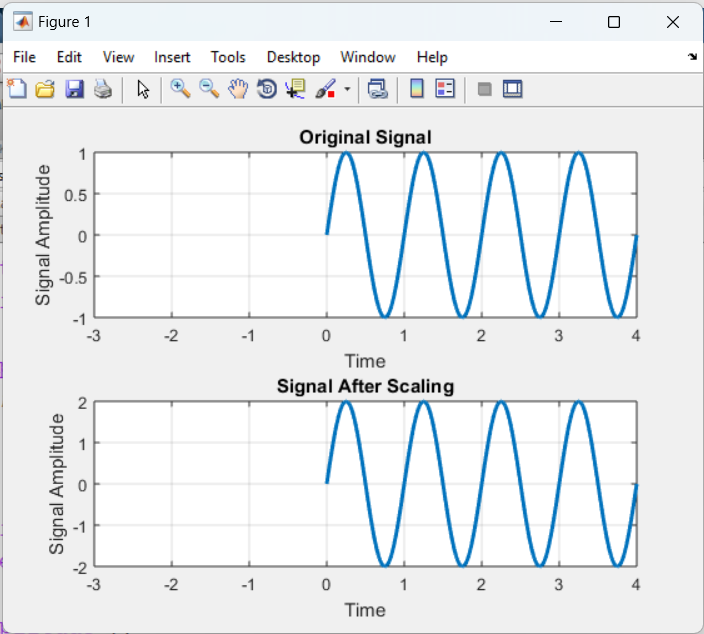
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**Task 6**

**Code:**

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**Output:**

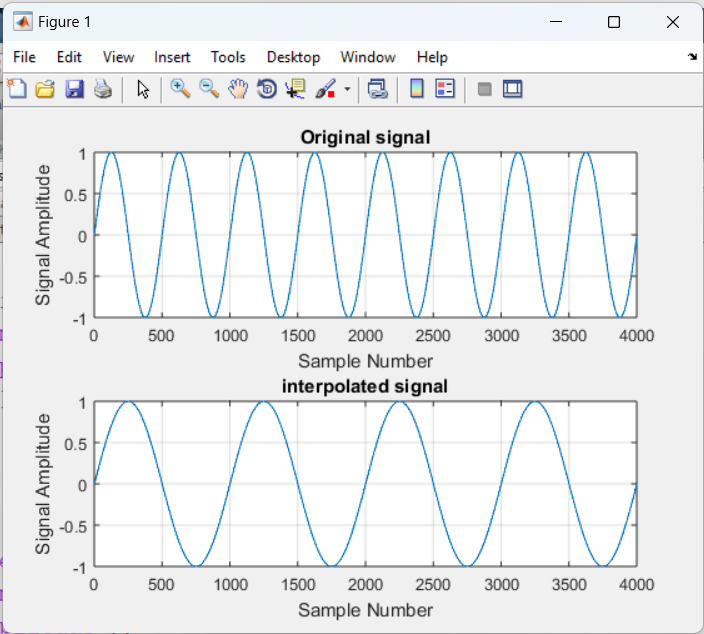
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**Task 7**

**Code:**

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**Output:**

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**Difference Practical implementation of Operations and theory:**

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